

SERIAL NO. 08/999,498

HARI.006USH

APPLICANT

Eliyahou Harari et al.

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GROUP

December 29, 1997

1714 N 2818

U. S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER								DATE	NAME	CLASS	SUB CLASS	FILING DATE
at	A1	3	8	9	5	3	6	0		7/15/75	Cricchi et al.	365	<i>2</i> 30.03	
ſ	A2	3	8	9	8	6	3	2		8/5/75	Spencer, Jr.	365	184	
	A3	3	9	0	6	4	5	5		9/16/75	Houston et al.	711	108	
	A4	3	9	1	4	7	5	0		10/21/75	Hadden, Jr.	365	184	
	A5	4	0	0	5	3	8	9		1/25/77	Penzel	711	173	
	A6	4	0	4	4	3	3	9		8/23/77	Berg	365	240	
	A7	4	0	5	8	7	9	9		11/15/77	George et al.	365	14	
	A8	4	0	6	4	4	0	5		12/20/77	Cricchi et al.	326	34	
	A9	4	0	9	0	2	5	8		5/16/78	Cricchi	365	184	
	A10	4	1	1	5	8	5	0		9/19/78	Houston et al.	707	1	
at	A11	4	1	3	0	8	9	0		12/19/78	Adam	365	184	

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			DOC	CUM	ENT	r N	UME	BER		DATE	COUNTRY	CLASS	SUB CLASS	TRANS.?
at	B1	0	2	8	3	2	3	8		9/21/88	Europe			<i>N</i> 0
1	B2	5	2	8	7	3	8			1/22/77	Japan			
	В3	5	8	8	6	7	7	7		5/24/83	Japan			
	В4	5	8	2	1	5	7	9	4	12/15/83	Japan			5-
act	B5	5	8	2	1	5	7	9	5	12/15/83	Japan			456

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.)

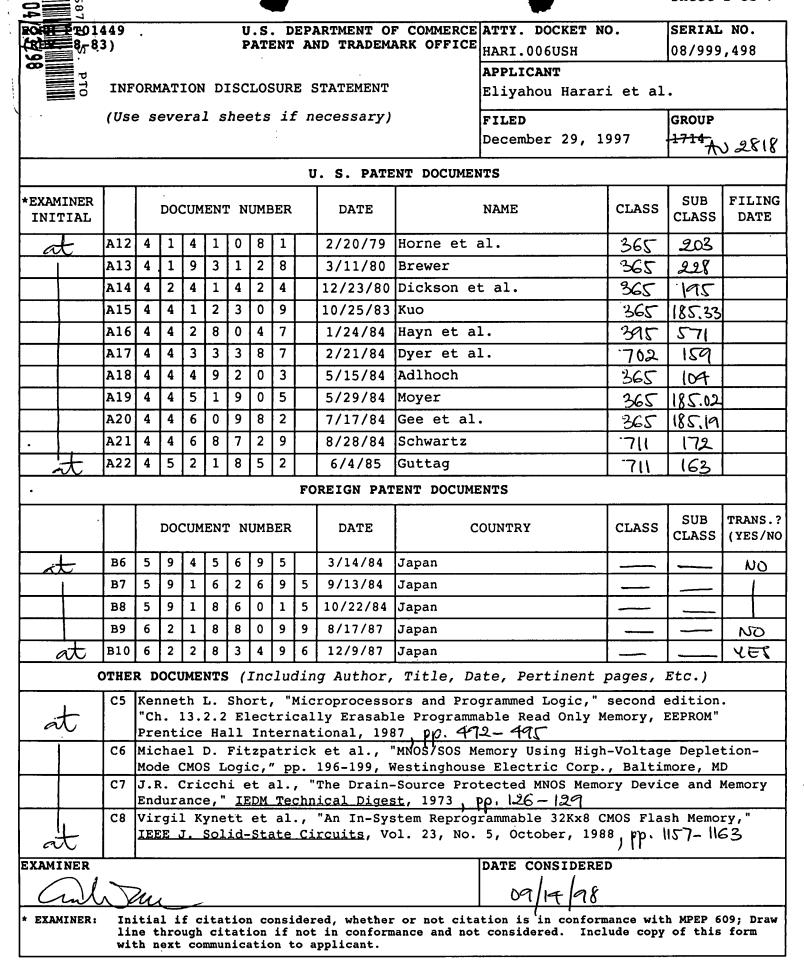
- C1 J.E. Brewer et al., "Block-Oriented Random Access MNOS Memory," National Computer Conference and Exposition, Chicago, May 6-10, 1974, AFIPS Conference Proceedings, Vol. 43, pp. 837-840 C2 J.E. Brewer et al., "Low Cost MNOS BORAM," Proceedings of IEEE National Aerospace Electronics Conference, NAECON '77, May 17-19, 1977 pp. 624-626 C3 J.R. Cricchi et al., "Nonvolatile Block-Oriented RAM," IEEE International Solid-State Circuits Conference, Feb. 13-15, 1974, Digest of Technical Papers pp. 204-205
 - C4 Raul-Adrian Cernea et al., "A 1Mb Flash EEPROM," 1989 IEEE International Solid-State Circuits Conference

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* EXAMINER:

Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



Sheet 3 of 7 FORM PE01449 . U.S. DEPARTMENT OF COMMERCE ATTY. DOCKET NO. SERIAL NO. (12年 8-83) PATENT AND TRADEMARK OFFICE HARI.006USH 08/999,498 APPLICANT INFORMATION DISCLOSURE STATEMENT Eliyahou Harari et al. (Use several sheets if necessary) FILED GROUP 1714 AU 2818 December 29, 1997 U. S. PATENT DOCUMENTS SUB FILING *EXAMINER DOCUMENT NUMBER DATE NAME CLASS CLASS DATE INITIAL A23 6/4/85 5 2 1 8 Guttag 711 163 at A24 4 5 2 2 5 7 7/2/85 Cricchi 184 365 A25 2 9 8 2/10/87 Rao 1852 A26 6 8 5 0 8/4/87 Canepa ३६८ 230.08 A27 7 5 8 9 8 6 7/19/88 Kuo 365 186 7/19/88 A28 4 7 5 8 9 8 8 Kuo 365 185.M A29 7 3 3 0 5 8/9/88 Kuo 4 6 365 185.22 A30 8 5 10/25/88 | Iida et al. 8 0 365 185,22 A31 7 9 6 2 3 5 1/3/89 Sparks et al. 365 189.07 A32 8 0 5 1 5 2/14/89 Terada et al. 371 21.1 2 3/7/89 A33 8 1 1 3 Knothe et al. 365 195 A34 1 2 3/7/89 Kobayashi et al. 365 185.22 2 2 A35 8 1 4/11/89 Nakamura et al. 195 A36 9 7 0 5 4 8 3 6/13/89 Tigelaar et al. 365 185.16 A37 4 Kreifels et al. 8 4 1 4 8 2 6/20/89 365 185.22 **A38** 2 2 8 0 8/22/89 |Carroll 6 365 185.09 FOREIGN PATENT DOCUMENTS SUB TRANS.? DOCUMENT NUMBER DATE COUNTRY CLASS CLASS (YES/NO B11 6 2 2 8 3 12/9/87 atJapan NO B12 6 3 1 8 5 9 6 1/26/88 Japan **B13** 3 1 0 6 9 5/12/88 6 8 Japan B14 7 3 1 8 3 7/29/88 Japan 3 2 B15 6 9 9/20/88 Japan 64 at

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.)

C9 Denny Cormier, "Erasable/Programmable Solid-State Memories," EDN, pp. 145-154, November 14, 1985

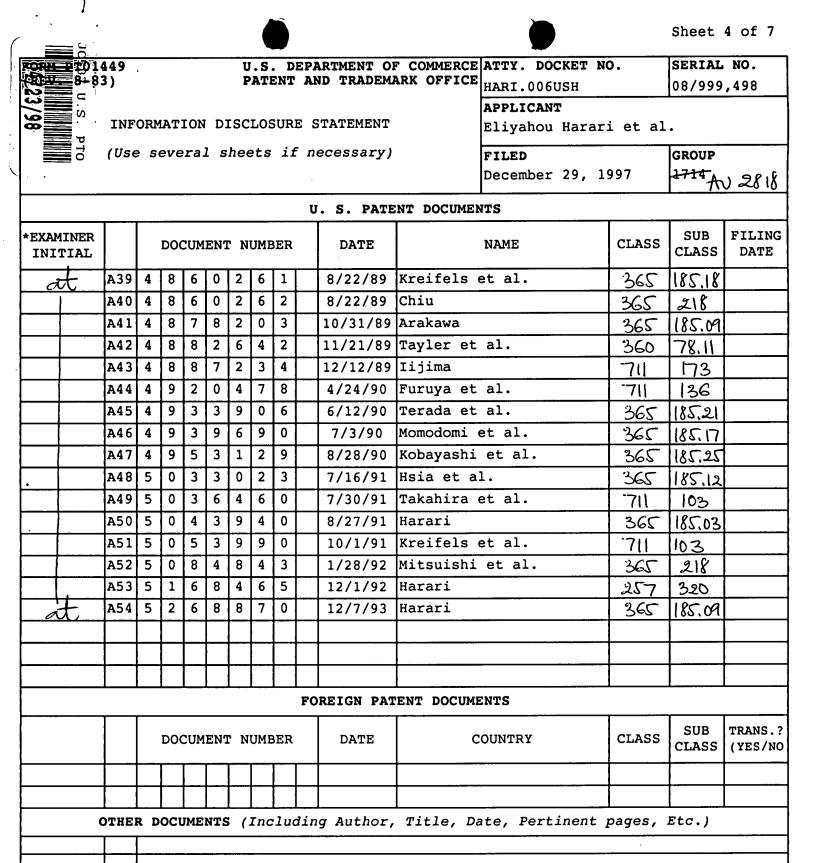
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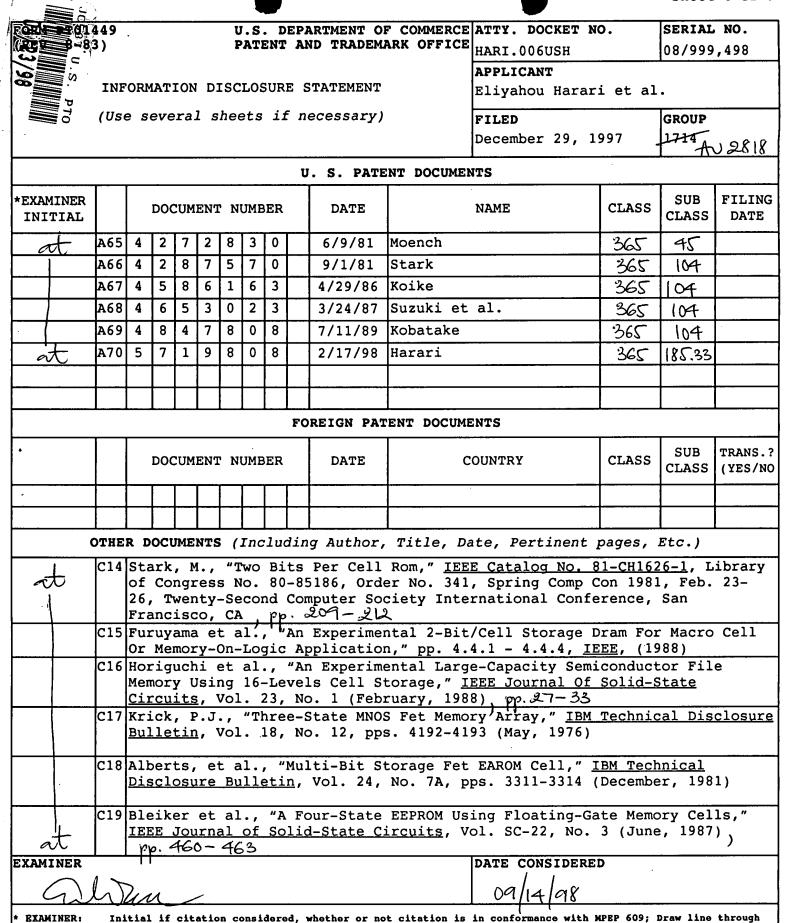
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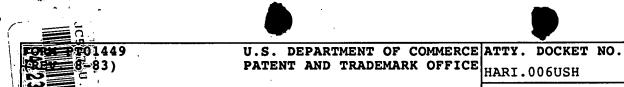
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at	A55	4	0	9	9	0	6	9		7/4/78	Cricc	hi et al.	365	218							
	A56	4	9	3	1	9	9	7		6/5/90	Mitsu	ishi et al.	365	185.04							
	A57	4	7	5	2	8	7	1		6/21/88	Spark	s et al.	365	185.11							
	A58	4	9	7	0	6	9	2		11/13/90	Ali e	t al.	365	230.06							
	A59	4	9	4	9	3	0	9		8/14/90	Rao		365	185.12							
	A60	5	4	1	8	7	5	2		5/23/95	Harar	i et al.	365	218							
	A61	5	0	9	5	3	4	4		3/10/92	Harar	i	257	328							
	A62	5	1	6	3	0	2	1		11/10/92	Mehro	tra et al.	365	185.03							
	A63	5	1	7	2	3	3	8		12/15/92	Mehro	tra et al.	365	185.03							
at	A64	5	4	3	4	8	2	5		7/18/95	Harar	i	365	185.24							
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4	C10											AND PROCESS- TO			PROM,						
at					985		of j	Soli	<u>d</u> :	State Circu	its, V	Vol. SC-20, No.	5, 198	5,							
	C11						tio	n, 2	7F2	256 256K (3	2K x 8	B) CMOS FLASH M	EMORY,	DATA SI	HEET,						
		May	y 1	988	, F	p.	1-	21				***									
	C12									., 48F512 5 p. 2-1 - 2-		LASH EEPROM - P	RELIMIN	ARY DAT	ra						
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at	A71	5	0	9	5	4	6	1	3/10/92	Miyakawa et al.		->	
	A72	5	2	7	0	9	7	9	12/14/93	Harari et al.			
	A73	5	2	7	0	9	8	0	12/14/93	Pathak et al.			
	A74	5	2	7	4	5	9	9	12/28/93	Ema			
	A75	5	2	9	7	0	9	6	3/22/94	Terada et al.			
	A76	5	3	8	4	7	4	3	1/24/95	Rouy			
	A77	5	3	8	8	0	8	3	2/7/95	Assar et al.			
at	A78	5	6	1	9	4	5	1	4/8/97	Costabello et al.			

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		DOCUMENT NUMBER								DATE	COUNTRY	CLASS	SUB CLASS	TRANS.? (YES/NO
at	B16	0	3	9	2	8	9	5		10/17/90	European Pat. Off.			NO
at	в17	4	1	1	9	3	9	4		12/19/91	Germany			No
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.)

C20 Nakayama et al. "A 60ns 16Mb Flash EEPROM With Program and Erase Sequence at Controller". IEEE Journal of Solid-State Circuits, Vol. 26, No. 11, Nov. 1991, New York US, pp. 1600-1605

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